HOME - PRODUCTS - SKY13489-001

SKY13489-001 0.7 To 2.7 GHz SPDT High Power Switch (Single Bit Control) In A WLCSP Package

OVERVIEW

SPECIFICATIONS

Overview

The SKY13489-001 is a single-pole, double-throw (SPDT) LTE/WCDMA/GSM transmit switch. Switching is controlled by an integrated GPIO interface with a single control pin. Depending on the logic voltage level applied to the control pin, the antenna port is connected to one of the switched RF outputs (RF1 or RF2) through a low insertion loss path, while the path between the antenna port and the other RF port is in a high isolation state.

No external DC blocking capacitors are required as long as no DC voltage is applied on any RF path.

The SKY13489-001 is provided in a compact 6-bump, $1.135 \times 0.735 \times 0.400$ mm Wafer Level Chip Scale Package (WLCSP) that meets requirements for board-level assembly. Bump diameters are 217 microns with a minimum bump pitch of 400 microns.

Features

Broadband frequency range: 0.7

to 2.7 GHz

J-STD-020)

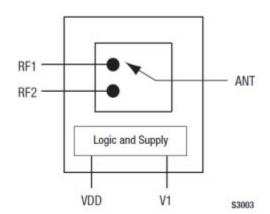
Low insertion loss: 0.3 dB @ 2.7

GHz

High isolation: 22 dB up to 2.7

GHz

No external DC blocking capacitors required Single GPIO control line with VDD voltage regulator: VCTL = 1.35 to 3.00 V VDD = 2.45 to 4.80 V Small, 6-bump WLCSP, 217 µm diameter, 400 µm pitch (1.135 x 0.735 x 0.400 mm) package (MSL1, 260 °C per JEDEC



Specifications

ProductName :	SKY13489-001
Description (Absorptive/ Reflective) :	SPDT (R)
Frequency (GHz) Min. :	0.7
Frequency (GHz) Max. :	2.7

Typ. IL (dB) :	0.25
Typ. Isol. (dB) :	32
Typ. IIP3 (dBm) :	Typ IMD3, -120
Typ. IP _{1 dB} (dBm) :	39
Package :	6 bump wICSP,
Package (mm) :	0.7 x 1.1 x 0.41
Product Information:	New Frequency Max.: ≥1 GHz and <2.9 GHz Frequency Min.: ≥400 MHz Function: SPDT / SP2T Package Type: WLCSP Status: New