



STGD6NC60HD

N-channel 600V - 7A - DPAK
Very fast PowerMESH™ IGBT

General features

| Type | V _{CES} | V _{CE(sat)} Max @25°C | I _C @100°C |
|-------------|------------------|-----------------------------------|--------------------------|
| STGD6NC60HD | 600V | <2.5V | 7A |

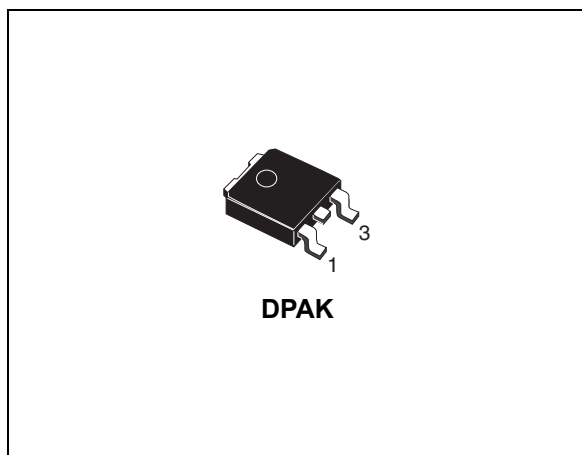
- Low on voltage drop (V_{cesat})
- Low C_{RES} / C_{IES} ratio (no cross-conduction susceptibility)
- Very soft ultra fast recovery antiparallel diode
- High frequency operation

Description

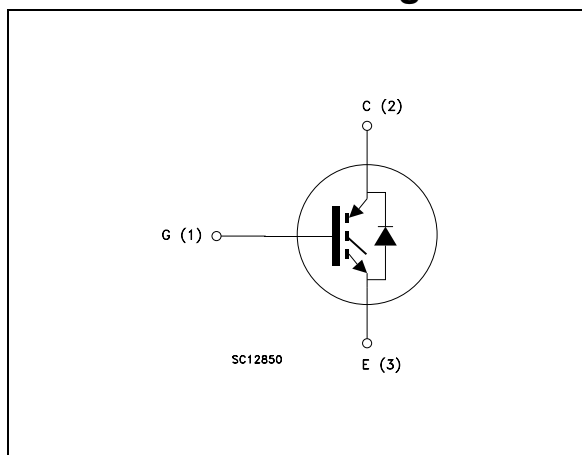
Using the latest high voltage technology based on a patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, the PowerMESH™ IGBTs, with outstanding performances. The suffix "H" identifies a family optimized for high frequency application in order to achieve very high switching performances (reduced t_{fall}) maintaining a low voltage drop.

Applications

- High frequency inverters
- SMPS and PFC in both hard switch and resonant topologies
- Motor drivers



Internal schematic diagram



Order codes

| Part number | Marking | Package | Packaging |
|---------------|-----------|---------|-------------|
| STGD6NC60HDT4 | GD6NC60HD | DPAK | Tape & reel |

Contents

| | | |
|----------|---|-----------|
| 1 | Electrical ratings | 3 |
| 2 | Electrical characteristics | 4 |
| | 2.1 Electrical characteristics (curves) | 7 |
| 3 | Test circuit | 10 |
| 4 | Package mechanical data | 11 |
| 5 | Packaging mechanical data | 13 |
| 6 | Revision history | 14 |

1 Electrical ratings

Table 1. Absolute maximum ratings

| Symbol | Parameter | Value | Unit |
|--------------------------------|--|-------------|------|
| V _{CES} | Collector-emitter voltage (V _{GS} = 0) | 600 | V |
| I _C ⁽¹⁾ | Collector current (continuous) at T _C = 25°C | 15 | A |
| I _C ⁽¹⁾ | Collector current (continuous) at T _C = 100°C | 7 | A |
| I _{CM} ⁽²⁾ | Collector current (pulsed) | 21 | A |
| V _{GE} | Gate-emitter voltage | ±20 | V |
| I _F | Diode RMS forward current at T _C =25°C | 10 | A |
| P _{TOT} | Total dissipation at T _C = 25°C | 56 | W |
| T _{stg} | Storage temperature | - 55 to 150 | °C |
| T _j | Operating junction temperature | | |
| T _l | Maximum lead temperature for soldering purpose (for 10sec. 1.6 mm from case) | 300 | °C |

1. Calculated according to the iterative formula:

$$I_C(T_C) = \frac{T_{JMAX} - T_C}{R_{THJ-C} \times V_{CESAT(MAX)}(T_C, I_C)}$$

2. Pulse width limited by max junction temperature

Table 2. Thermal resistance

| Symbol | Parameter | Value | Unit |
|-----------------------|---|-------|------|
| R _{thj-case} | Thermal resistance junction-case max | 2 | °C/W |
| R _{thj-amb} | Thermal resistance junction-ambient max | 62.5 | °C/W |

2 Electrical characteristics

($T_{CASE}=25^{\circ}C$ unless otherwise specified)

Table 3. Static

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|---------------|---|---|------|------------|-----------|---------------|
| $V_{BR(CES)}$ | Collector-emitter breakdown voltage | $I_C = 1mA, V_{GE} = 0$ | 600 | | | V |
| $V_{CE(sat)}$ | Collector-emitter saturation voltage | $V_{GE} = 15V, I_C = 3A$ $V_{GE} = 15V, I_C = 3A, T_C = 125^{\circ}C$ | | 1.9 1.7 | 2.5 | V V |
| $V_{GE(th)}$ | Gate threshold voltage | $V_{CE} = V_{GE}, I_C = 250 \mu A$ | 3.75 | | 5.75 | V |
| I_{CES} | Collector cut-off current ($V_{GE} = 0$) | $V_{CE} = \text{Max rating}, T_C = 25^{\circ}C$ $V_{CE} = \text{Max rating}, T_C = 125^{\circ}C$ | | | 10 1 | μA mA |
| I_{GES} | Gate-emitter leakage current ($V_{CE} = 0$) | $V_{GE} = \pm 20V, V_{CE} = 0$ | | | ± 100 | nA |
| g_{fs} | Forward transconductance | $V_{CE} = 15V, I_C = 3A$ | | 3 | | S |

Table 4. Dynamic

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|-----------|------------------------------|---|------|------|------|------|
| C_{ies} | Input capacitance | $V_{CE} = 25V, f = 1MHz,$ $V_{GE} = 0$ | | 205 | | pF |
| C_{oes} | Output capacitance | | | 32 | | pF |
| C_{res} | Reverse transfer capacitance | | | 5.5 | | pF |
| Q_g | Total gate charge | $V_{CE} = 390V, I_C = 3A,$ $V_{GE} = 15V,$ <i>(see Figure 17)</i> | | 13.6 | | nC |
| Q_{ge} | Gate-emitter charge | | | 3.4 | | nC |
| Q_{gc} | Gate-collector charge | | | 5.1 | | nC |
| I_{CL} | Turn-off SOA minimum current | $V_{clamp} = 390V, T_j = 150^{\circ}C,$ $R_G = 10\Omega, V_{GE} = 15V$ | | 19 | | A |

Table 5. Switching on/off (inductive load)

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|---|---|---|------|------------------|------|------------------------|
| $t_{d(on)}$ t_r $(di/dt)_{on}$ | Turn-on delay time Current rise time Turn-on current slope | $V_{CC} = 390V, I_C = 3A$ $R_G = 10\Omega, V_{GE} = 15V,$ $T_j = 25^\circ C$ <i>(see Figure 18)</i> | | 12 5 612 | | ns ns A/ μs |
| $t_{d(on)}$ t_r $(di/dt)_{on}$ | Turn-on delay time Current rise time Turn-on current slope | $V_{CC} = 390V, I_C = 3A$ $R_G = 10\Omega, V_{GE} = 15V,$ $T_j = 125^\circ C$ <i>(see Figure 18)</i> | | 13 4.3 560 | | ns ns A/ μs |
| $t_r(V_{off})$ $t_{d(off)}$ t_f | Off voltage rise time Turn-off delay time Current fall time | $V_{CC} = 390V, I_C = 3A,$ $R_{GE} = 10\Omega, V_{GE} = 15V, T_j = 25^\circ C$ <i>(see Figure 18)</i> | | 40 76 100 | | ns ns ns |
| $t_r(V_{off})$ $t_{d(off)}$ t_f | Off voltage rise time Turn-off delay time Current fall time | $V_{CC} = 390V, I_C = 3A,$ $R_{GE} = 10\Omega, V_{GE} = 15V,$ $T_j = 125^\circ C$ <i>(see Figure 18)</i> | | 60 98 124 | | ns ns ns |

Table 6. Switching energy (inductive load)

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|---|---|---|------|-----------------|------|-------------------------------|
| $E_{on}^{(1)}$ $E_{off}^{(2)}$ E_{ts} | Turn-on switching losses Turn-off switching losses Total switching losses | $V_{CC} = 390V, I_C = 3A$ $R_G = 10\Omega, V_{GE} = 15V,$ $T_j = 25^\circ C$ <i>(see Figure 18)</i> | | 20 68 88 | | μJ μJ μJ |
| $E_{on}^{(1)}$ $E_{off}^{(2)}$ E_{ts} | Turn-on switching losses Turn-off switching losses Total switching losses | $V_{CC} = 390V, I_C = 3A$ $R_G = 10\Omega, V_{GE} = 15V,$ $T_j = 125^\circ C$ <i>(see Figure 18)</i> | | 37 93 130 | | μJ μJ μJ |

1. E_{on} is the turn-on losses when a typical diode is used in the test circuit in figure 2. If the IGBT is offered in a package with a co-pak diode, the co-pak diode is used as external diode. IGBTs & Diode are at the same temperature (25°C and 125°C)
2. Turn-off losses include also the tail of the collector current

Table 7. Collector-emitter diode

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|-----------|--------------------------|---|------|------|------|------|
| V_f | Forward on-voltage | $I_f = 1.5A$ | | 1.6 | 2.1 | V |
| | | $I_f = 1.5A, T_j = 125^\circ C$ | | 1.3 | | V |
| t_{rr} | Reverse recovery time | $I_f = 3A, V_R = 40V,$ | | 21 | | ns |
| Q_{rr} | Reverse recovery charge | $T_j = 25^\circ C, di/dt = 100 A/\mu s$ | | 14 | | nC |
| I_{rrm} | Reverse recovery current | (see Figure 19) | | 1.36 | | A |
| t_{rr} | Reverse recovery time | $I_f = 3A, V_R = 40V,$ | | 34 | | ns |
| Q_{rr} | Reverse recovery charge | $T_j = 125^\circ C, di/dt = 100A/\mu s$ | | 32 | | nC |
| I_{rrm} | Reverse recovery current | (see Figure 19) | | 1.88 | | A |

2.1 Electrical characteristics (curves)

Figure 1. Output characteristics

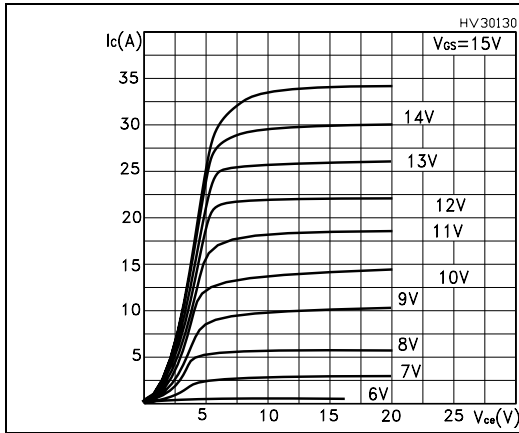


Figure 2. Transfer characteristics

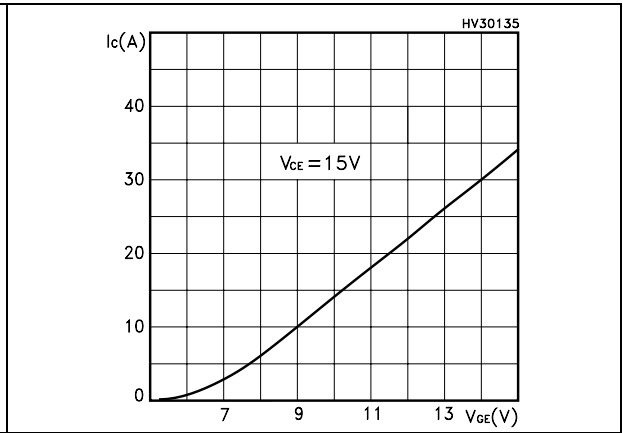


Figure 3. Transconductance

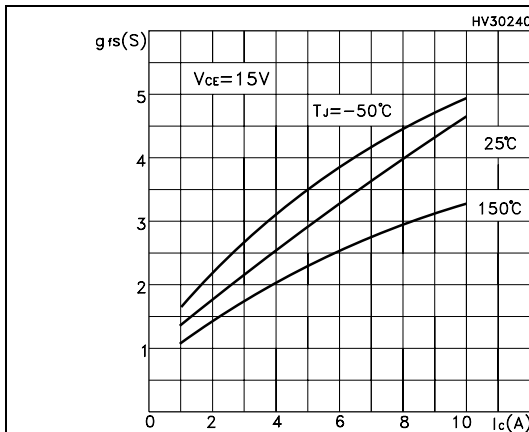


Figure 4. Collector-emitter on voltage vs temperature

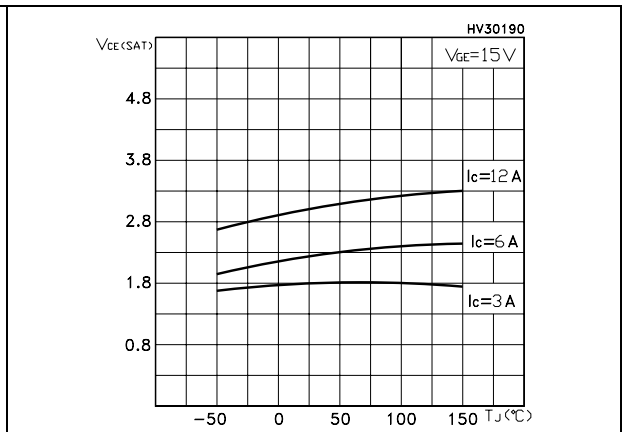


Figure 5. Gate charge vs gate-source voltage

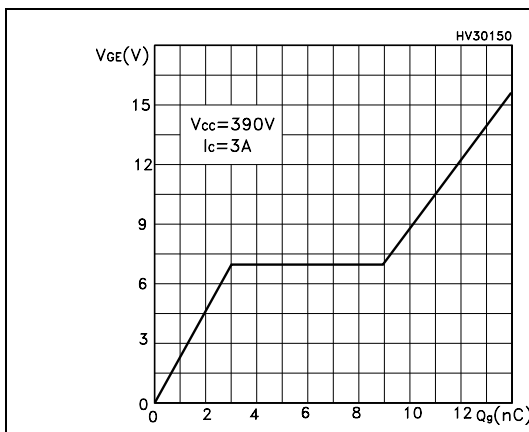


Figure 6. Capacitance variations

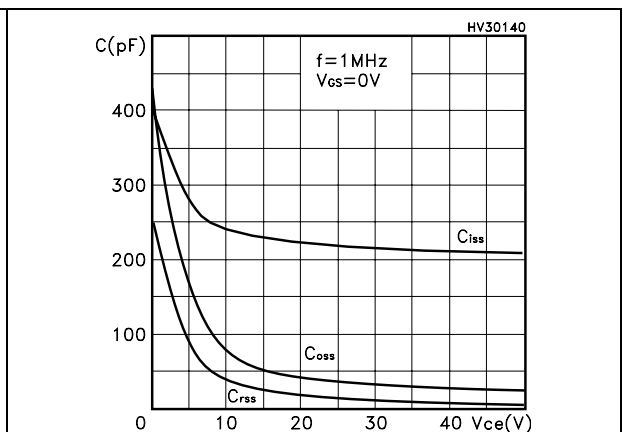


Figure 7. Normalized gate threshold voltage vs temperature

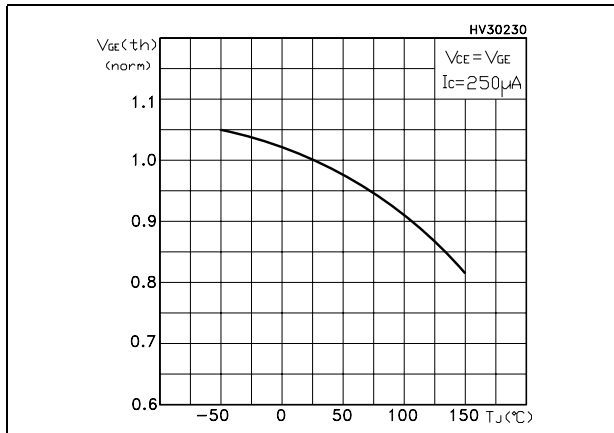


Figure 8. Collector-emitter on voltage vs collector current

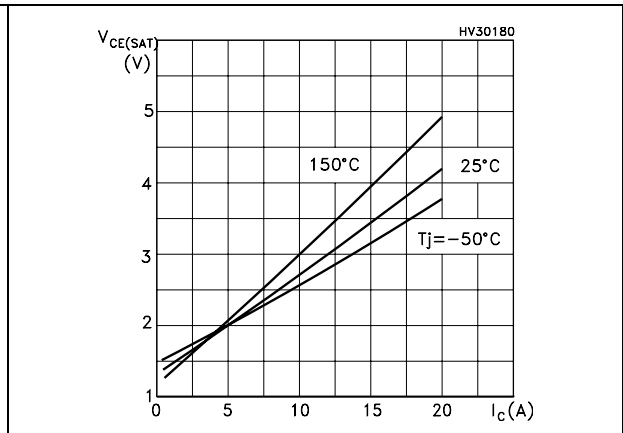


Figure 9. Normalized breakdown voltage vs temperature

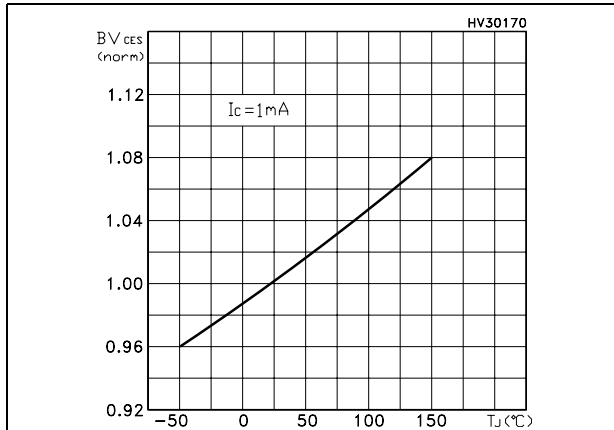


Figure 10. Switching losses vs temperature

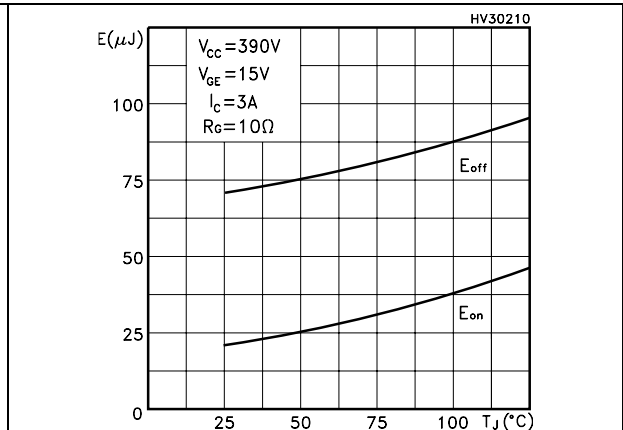


Figure 11. Switching losses vs gate resistance

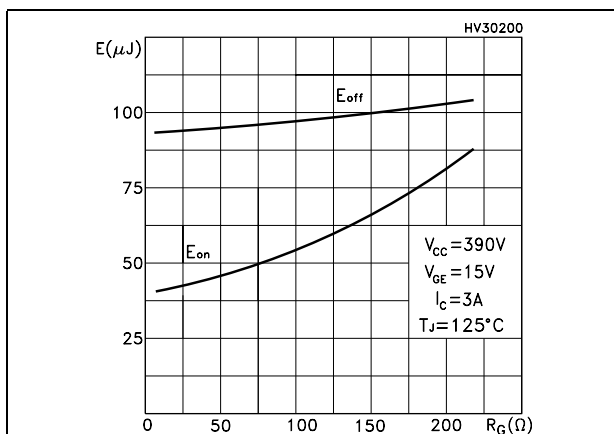


Figure 12. Switching losses vs collector current

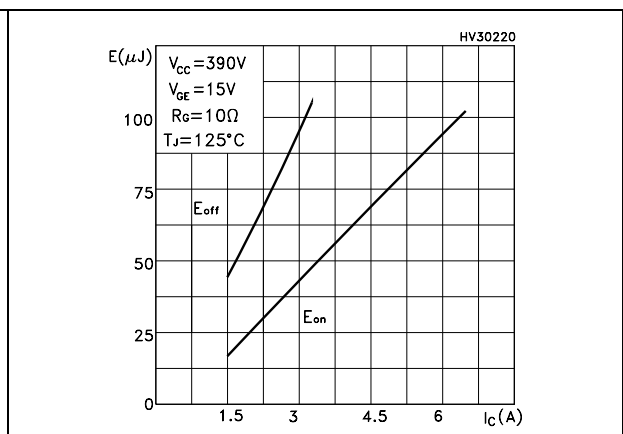


Figure 13. Thermal Impedance

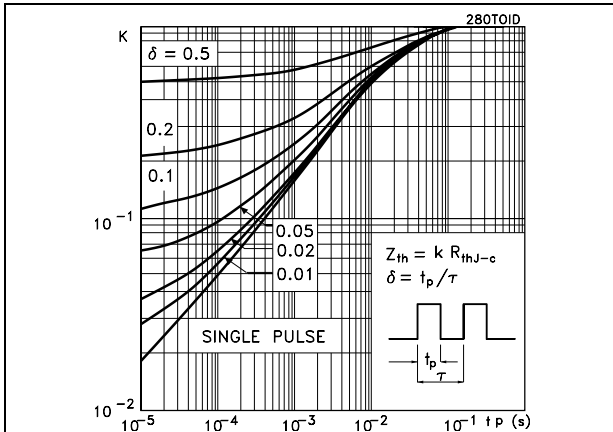


Figure 14. Turn-off SOA

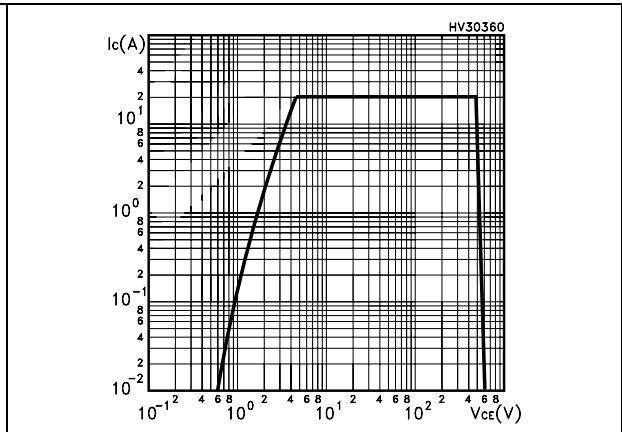
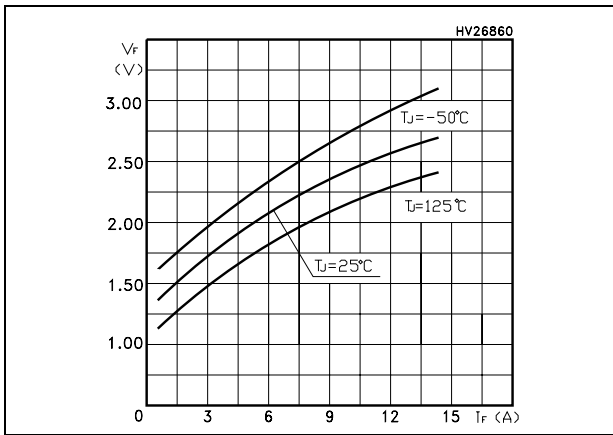


Figure 15. Emitter-collector diode characteristics



3 Test circuit

Figure 16. Test circuit for inductive load switching

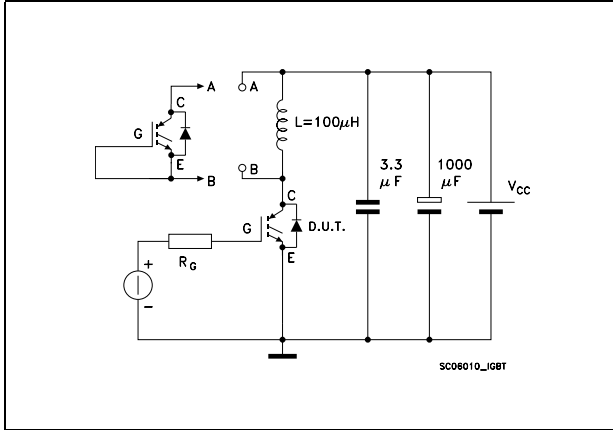


Figure 17. Gate charge test circuit

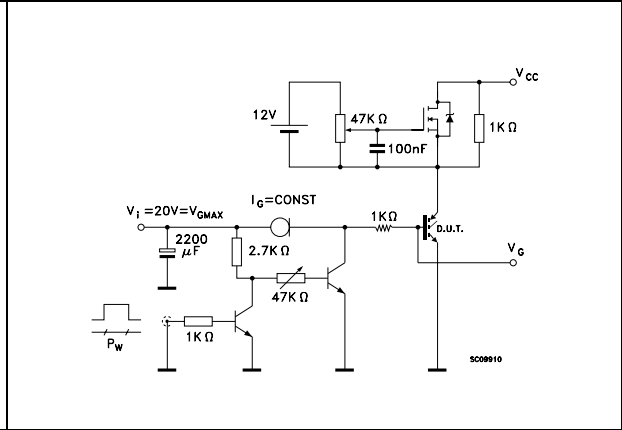


Figure 18. Switching waveform

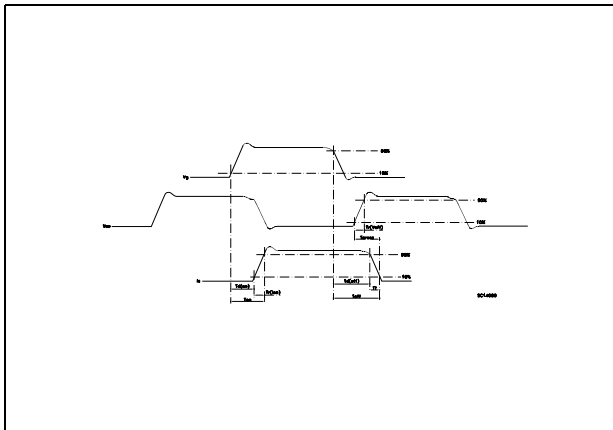
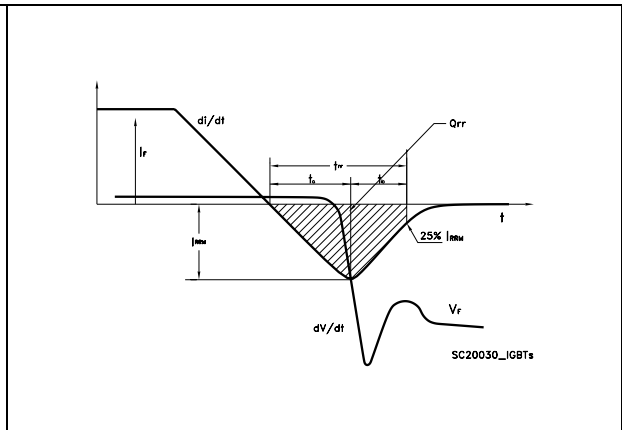


Figure 19. Diode recovery time waveform

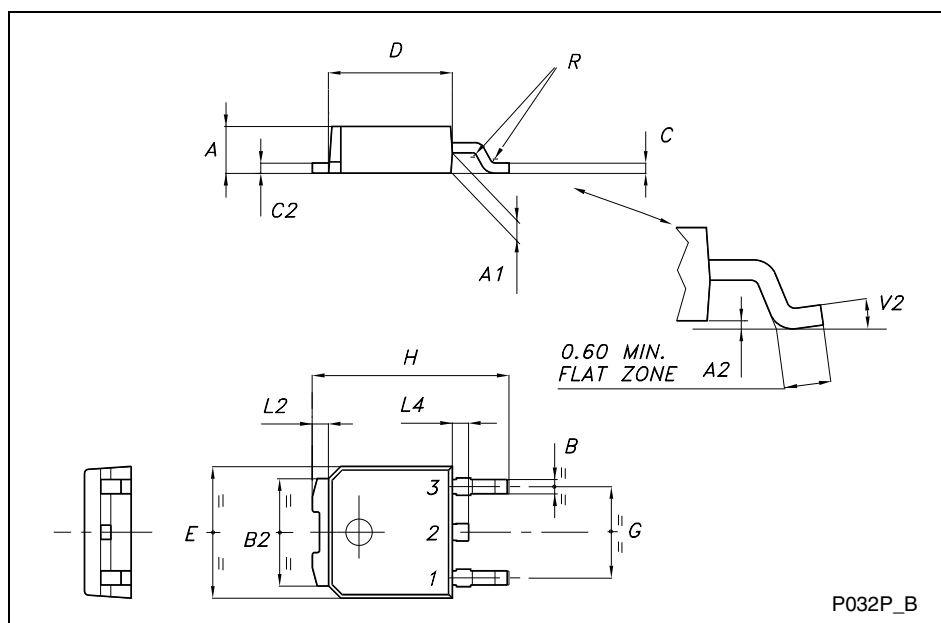


4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

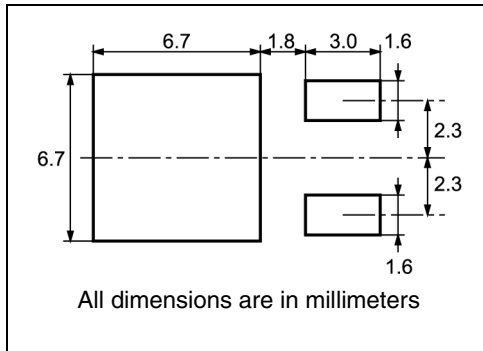
TO-252 (DPAK) MECHANICAL DATA

| DIM. | mm | | | inch | | |
|------|------|------|-------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| A | 2.20 | | 2.40 | 0.087 | | 0.094 |
| A1 | 0.90 | | 1.10 | 0.035 | | 0.043 |
| A2 | 0.03 | | 0.23 | 0.001 | | 0.009 |
| B | 0.64 | | 0.90 | 0.025 | | 0.035 |
| B2 | 5.20 | | 5.40 | 0.204 | | 0.213 |
| C | 0.45 | | 0.60 | 0.018 | | 0.024 |
| C2 | 0.48 | | 0.60 | 0.019 | | 0.024 |
| D | 6.00 | | 6.20 | 0.236 | | 0.244 |
| E | 6.40 | | 6.60 | 0.252 | | 0.260 |
| G | 4.40 | | 4.60 | 0.173 | | 0.181 |
| H | 9.35 | | 10.10 | 0.368 | | 0.398 |
| L2 | | 0.8 | | | 0.031 | |
| L4 | 0.60 | | 1.00 | 0.024 | | 0.039 |
| V2 | 0° | | 8° | 0° | | 0° |



5 Packaging mechanical data

DPAK FOOTPRINT



TAPE AND REEL SHIPMENT

40 mm min. Access hole at slot location

Full radius

Tape slot in core for tape start 2.5mm min. width

| DIM. | mm | | inch | |
|------|------|------|-------|--------|
| | MIN. | MAX. | MIN. | MAX. |
| A | | 330 | | 12.992 |
| B | 1.5 | | 0.059 | |
| C | 12.8 | 13.2 | 0.504 | 0.520 |
| D | 20.2 | | 0.795 | |
| G | 16.4 | 18.4 | 0.645 | 0.724 |
| N | 50 | | 1.968 | |
| T | | 22.4 | | 0.881 |

| BASE QTY | BULK QTY |
|----------|----------|
| 2500 | 2500 |

| DIM. | mm | | inch | |
|------|------|------|-------|-------|
| | MIN. | MAX. | MIN. | MAX. |
| A0 | 6.8 | 7 | 0.267 | 0.275 |
| B0 | 10.4 | 10.6 | 0.409 | 0.417 |
| B1 | | 12.1 | | 0.476 |
| D | 1.5 | 1.6 | 0.059 | 0.063 |
| D1 | 1.5 | | 0.059 | |
| E | 1.65 | 1.85 | 0.065 | 0.073 |
| F | 7.4 | 7.6 | 0.291 | 0.299 |
| K0 | 2.55 | 2.75 | 0.100 | 0.108 |
| P0 | 3.9 | 4.1 | 0.153 | 0.161 |
| P1 | 7.9 | 8.1 | 0.311 | 0.319 |
| P2 | 1.9 | 2.1 | 0.075 | 0.082 |
| R | 40 | | 1.574 | |
| W | 15.7 | 16.3 | 0.618 | 0.641 |

TOP COVER TAPE

User Direction of Feed

Center line of cavity

Bending radius R min.

FEED DIRECTION

For machine ref. only including draft and radii concentric around B0

10 pitches cumulative tolerance on tape +/- 0.2 mm

6 Revision history

Table 8. Revision history

| Date | Revision | Changes |
|-------------|-----------------|-----------------------------------|
| 04-Aug-2005 | 1 | First release |
| 07-Mar-2006 | 2 | Complete version |
| 07-Feb-2007 | 3 | The document has been reformatted |

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2007 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com