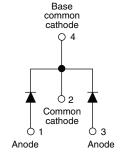


### Vishay Semiconductors

# Schottky Rectifier, 2 x 6 A





D-PAK (TO-252AA)	)
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PRODUCT SUMMARY						
Package	D-PAK (TO-252AA)					
I <sub>F(AV)</sub>	2 x 6 A					
V <sub>R</sub>	60 V					
V <sub>F</sub> at I <sub>F</sub>	0.57 V					
I <sub>RM</sub>	35 mA at 125 °C					
T <sub>J</sub> max.	150 °C					
Diode variation	Common cathode					
E <sub>AS</sub>	7 mJ					

#### **FEATURES**

- Popular D-PAK outline
- Center tap configuration



- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- $\bullet$  Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

#### **DESCRIPTION**

The VS-12CWQ06FNPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	12	A						
V <sub>RRM</sub>		60	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	320	A						
V <sub>F</sub>	6 Apk, T <sub>J</sub> = 125 °C (per leg)	0.57	V						
T <sub>J</sub>	Range	- 55 to 150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-12CWQ06FNPbF	UNITS					
Maximum DC reverse voltage	V <sub>R</sub>	60	V					
Maximum working peak reverse voltage	V <sub>RWM</sub>	60	V					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average per leg forward current See fig. 5 per device			I <sub>F(AV)</sub> 50 % duty cycle at T <sub>C</sub> = 131 °C, rectangular waveform		6	А			
		I <sub>F(AV)</sub>	30 % duty cycle at 1°C = 131 °C	12					
Maximum peak one cycle		I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	320	А			
See fig. 7	non-repetitive surge current See fig. 7		10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	105				
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.2 A, L = 10 mH		7	mJ			
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		0.8	Α			

# VS-12CWQ06FNPbF

# Vishay Semiconductors

## Schottky Rectifier, 2 x 6 A



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Revision: 14-Jan-11

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		6 A	T <sub>.1</sub> = 25 °C	0.61	V			
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	12 A	11 = 25 0	0.79				
See fig. 1	VFM (*)	6 A	T <sub>.1</sub> = 125 °C	0.57				
3		12 A	1J = 125 C	0.72				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	3	mA			
See fig. 2	'RM\'	$T_J = 125  ^{\circ}C$		35	111/4			
Threshold voltage	V <sub>F(TO)</sub>	$T_{.1} = T_{.1}$ maximum	0.36	V				
Forward slope resistance	r <sub>t</sub>	ij=ijiiiaxiiiiuiii	24.14	mΩ				
Typical junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal ra	360	pF				
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5	mm from package body	5.0	nH			

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 55 to 150	°C				
Maximum thermal resistance,	per leg	D	DC operation	3.0	°C/W				
junction to case	per device	$R_{thJC}$	See fig. 4	1.5	C/VV				
Annyayina ata waisht				0.3	g				
Approximate weight				0.01	OZ.				
Marking device			Case style D-PAK (similar to TO-252AA)	12CW	Q06FN				

#### Note

(1) 
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink



#### Schottky Rectifier, 2 x 6 A

## Vishay Semiconductors

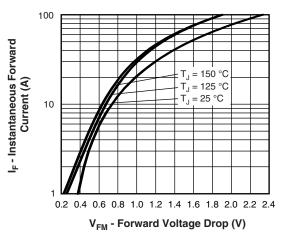


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

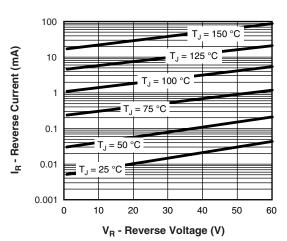


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

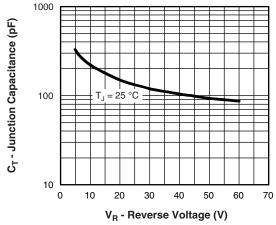


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

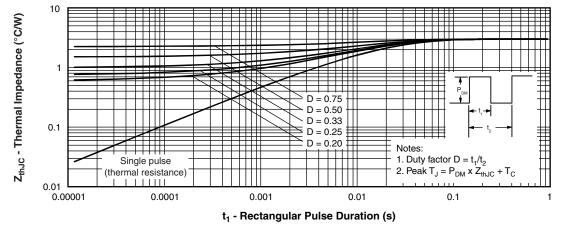


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

# Vishay Semiconductors

### Schottky Rectifier, 2 x 6 A



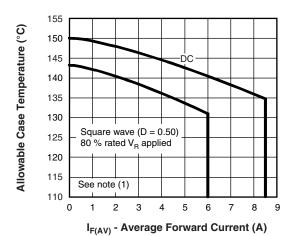


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

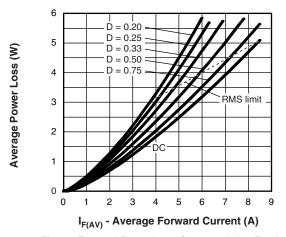


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

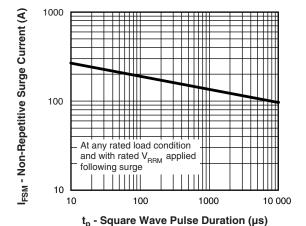


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 

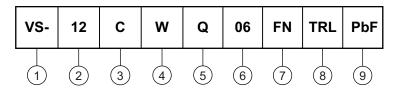


### Schottky Rectifier, 2 x 6 A

Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- 2 Current rating (12 A)
- Center tap configuration
- Package identifier:
  - W = D-PAK
- 5 Schottky "Q" series
- Voltage rating (06 = 60 V)
- 7 FN = TO-252AA
- None = Tube (50 pieces)
  - TR = Tape and reel
  - TRL = Tape and reel (left oriented)
  - TRR = Tape and reel (right oriented)
- 9 PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95059					
Packaging information	www.vishay.com/doc?95033					
SPICE model	www.vishay.com/doc?95278					



## Vishay Semiconductors

**NOTES** 

3

2

MAX.

0.410

0.070

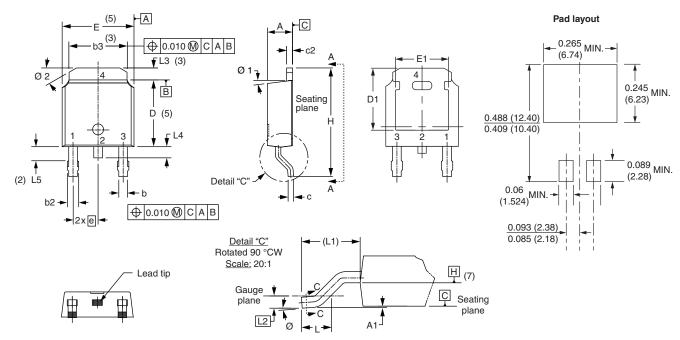
0.050

0.040

0.060

# **D-PAK (TO-252AA)**

#### **DIMENSIONS** in millimeters and inches



Ī	SYMBOL	MILLIMETERS		INCHES		NOTES	CYMPOL	MILLIMETERS		INCHES		
	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	SYMBOL	MIN.	MAX.	MIN.	MAX	
ſ	Α	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC
ſ	A1	-	0.13		0.005			Н	9.40	10.41	0.370	0.41
Ī	b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.07
Ī	b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.
ſ	b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC
Ī	С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.05
Ī	c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.04
ſ	D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.06
Ī	D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°
ſ	Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°
Ī	E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



## **Legal Disclaimer Notice**

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